

SX7CL

HCMOS SURFACE MOUNT CRYSTAL CLOCK OSCILLATOR

FEATURES

- SMD package
- Ultra low current consumption
- Applications : Hand-held consumer electronics

7.0 x 5.0 x 1.5 mm



Item	Specification			
Frequency Range	0.25 MHz - 50.0 MHz			
Output Logic	CMOS			
Overall Frequency Stability *	±25 ppm ~ ± 100 ppm (see options)			
Operating Temperature Range	0~+70°C commercial application (see options) -40~+85°C industrial application (see options)			
Supply Voltage Vdd		+1.8V ±5%	+2.5V ±5%	+3.3V ±5%
Supply Current Idd	<27 MHz	1.5 mA typ.	2.2 mA typ.	3.0 mA typ.
	>27MHz	2.5 mA typ	4.0 mA typ.	5.0 mA typ.
Output Level	VOH ≥ 0.9 Vdd		VOL ≤ 0.1 Vdd	
Output Load	15 pF			
Symmetry	45 / 55%			
Rise Time / Fall Time Fr/Ff	4 ~ 8 ns			
Tri-state function	pin #1 = high or open pin #1 = low		pin #3 ==> oscillation pin #3 ==> high impedance	
Start-up Time	10 ms max			
RMS Jitter (12 kHz to 20 MHz band)	1 ps max.			
Packing Unit	1000pcs/reel			
Soldering Condition	260°C, 10 sec x2max			
	Customer specifications on request			

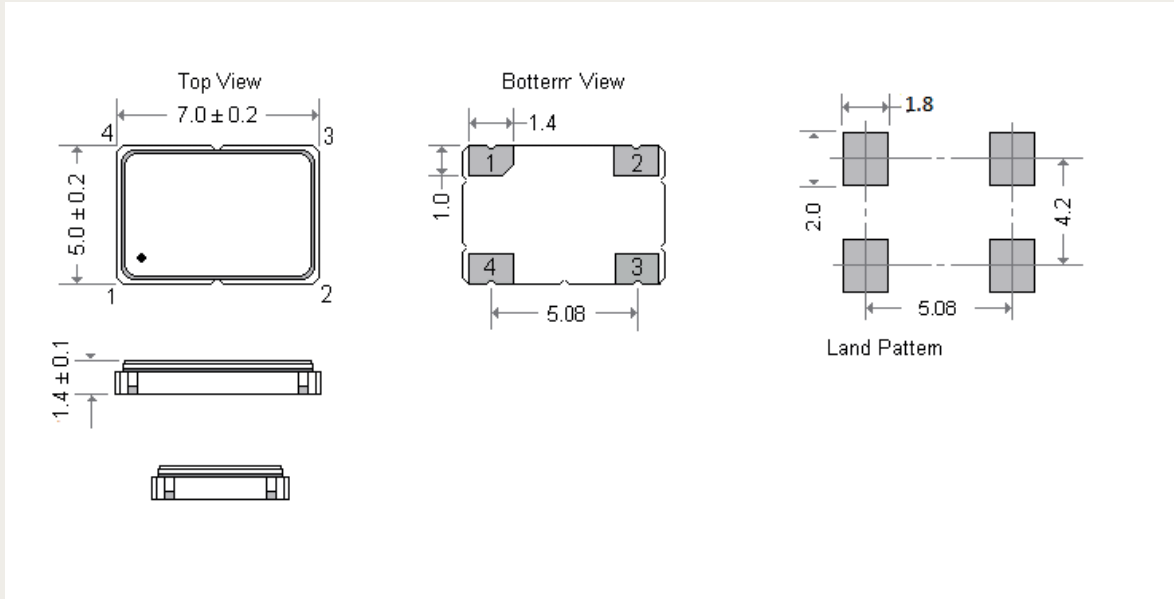
(*) Includes initial tolerance @+25°C, stability over operating temperature, stability vs. load change, stability vs. supply change and one year aging

OPTIONS & ORDERING INFORMATION

SX7CL				 MHz
Supply Voltage	Operating Temp. *	Overall Stability *	Tri-state Function	Output Load *	Frequency in MHz
18 = +1.8V	E = 0°/+70°C	25 = ±25 ppm	E = Tri-state	Blanc = 15 pF	Please specify the frequency in MHz
25 = +2.5V	F = -20°/+70°C	30 = ±30 ppm			
33 = +3.3V	K = -40°/+85°C	50 = ±50 ppm			
		100 = ±100 ppm			

* Note: Not all combinations are possible, please consult us.

OUTLINE DIMENSIONS



Pin Connections

#1 : E/D

#2 : GND

#3 : Output

#4 : Vdd