

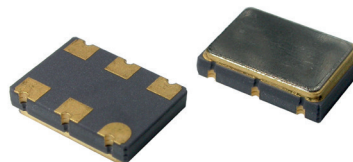
SX7LJK

LVDS SURFACE MOUNT CRYSTAL CLOCK OSCILLATOR

FEATURES

- Standard miniature package
- Femto second integrated phase jitter, 50 fs typical
- Superior phase noise

7.0 x 5.0 x 1.8 mm



Item	Specification		
Frequency Range	100 MHz ~ 250 MHz		
Output Signal	LVDS		
Overall Frequency Stability*	± 25 ppm ~ ± 100 ppm (see options)		
Operating Temperature Range	0 ~ +70°C commercial application (see options) -40 ~ +85°C industrial application (see options)		
Supply Voltage Vdd	+1.8V ±5%	+2.5V ±5%	+3.3V ±5%
Supply Current Idd	22 mA typ. ; 30 mA max.		
Output Voltage HIGH VOH	1.4 V typ. ; 1.6 V max.		
Output Voltage LOW VOL	1.10 V typ. ; 0.9 V min.		
Output Load	50 ohm from each output		
Symmetry	45/ 55%		
Rise Time/Fall Time Fr/Ff	0.15 ns typ. ; 0.3 ns max.		
Tri-state function	pin #1 = high or open pin #1 = low	pin #4 - #5 ==> oscillation pin #4 - #5 ==> disable	
Start-up Time	1 ms typ. ; 5 ms max.		
RMS Phase Jitter (12 kHz to 20 MHz)	50 fs typ. , 300 fs max		
Phase Noise (typical)	Offset 100 Hz 1 kHz 10 kHz 100 kHz 1 MHz 10 MHz	Frequency 125.000 MHz -114 dBc / Hz -135 dBc / Hz -147 dBc / Hz -157 dBc / Hz -163 dBc / Hz -164 dBc / Hz	
Packing Unit	1000pcs / reel		
Soldering Condition	260°C , 10 sec x2 max		
Customer specifications on request			

(*) Includes initial tolerance@+25°C, stability over operating temperature, stability vs. load change, stability vs. supply change and one year aging

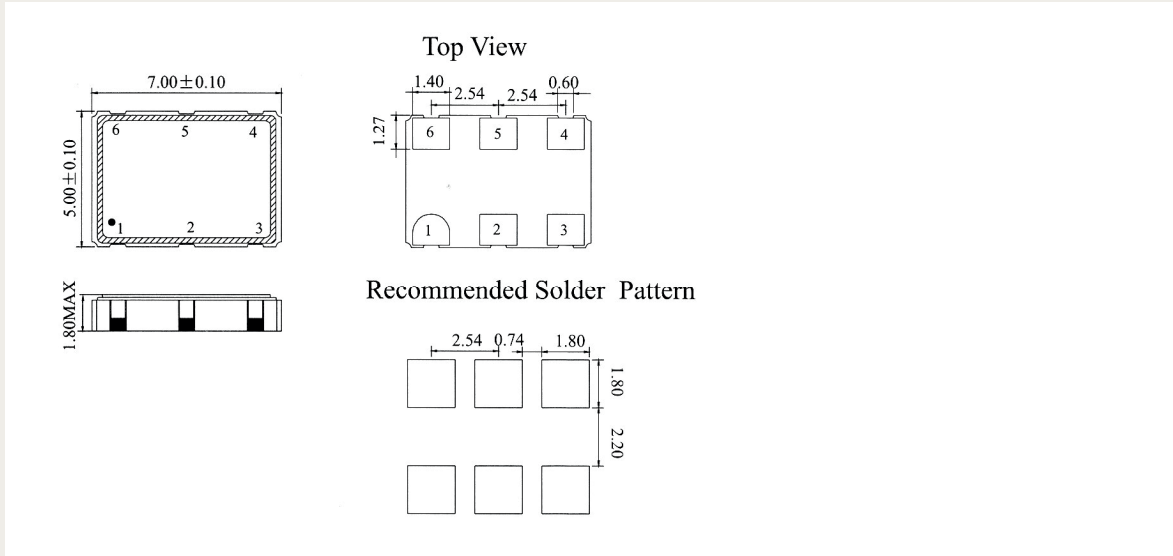
OPTIONS & ORDERING INFORMATION

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..... MHz
Supply Voltage *	Operating Temp. *	Overall Stability *	Tri-state Function	Frequency in MHz
18 = +1.8V	E = 0°/+70°C	25 = ±25 ppm	E = Tri-state	Please specify the frequency in MHz
25 = +2.5V	F = -20°/+70°C	30 = ±30 ppm		
33 = +3.3V	K = -40°/+85°C	50 = ±50 ppm		
		100 = ±100 ppm		

* Note : Not all combinations are possible, please consult us.

OUTLINE DIMENSIONS (mm)



Pin Connections

#1 : E/D

#4 : Output

#2 : NC

#5 : Complementary output

#3: GND

#6: Vdd