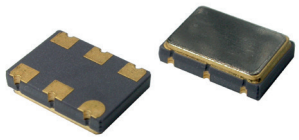
SX7EJK LVPECL SURFACE MOUNT CRYSTAL CLOCK OSCILLATOR

FEATURES

- SMD package
- Femto second integrated phase jitter, 50 fs typical
- Superior phase noise

7.0 x 5.0 x 1.8 mm



Item	Specification	
Frequency Range	100 MHz ~ 250 MHz	
Output Signal	LVPECL	
Overall Frequency Stability*	± 25 ppm ~ ± 100 ppm (see options)	
Operating Temperature Range	0 ~ +70°C commercial application (see options) -40 ~ +85°C industrial application (see options)	
Supply Voltage Vdd	+2.5V ±5%	+3.3V ±5%
Supply Current Idd	52 mA typ. ; 65 mA max	
Output Voltage HIGH VOH	Vdd -1.085 V min. ; Vdd -0.86 V max	
Output Voltage LOW VOL	Vdd -1.81 V min. ; Vdd -1.62 V max	
Output Load	50 ohm to Vdd-2V	
Symmetry	45/ 55%	
Rise Time/Fall Time Fr/Ff	0.15 ns typ. , 0.4 ns max.	
Tri-state function	pin #1 = high or open pin #1 = low	pin #4 - #5 ==> oscillation pin #4 - #5 ==> disable
Start-up Time	3 ms typ. ; 10 ms max.	
RMS Phase Jitter (12 kHz to 20 MHz)	50 fs typ. , 300 fs max	
Phase Noise (typical)	**Offset** 100 Hz 1 kHz 10 kHz 100 kHz 1 MHz 10 MHz	**Frequency 125.000 MHz** -114 dBc / Hz -135 dBc / Hz -147 dBc / Hz -157 dBc / Hz -163 dBc / Hz -164 dBc / Hz
Packing Unit	1000pcs / reel	
Soldering Condition	260°C , 10 sec x2 max	
	Customer specifications on request	

(*) Includes initial tolerance @+25°C, stability over operating temperature, stability vs. load change, stability vs. supply change and one year aging

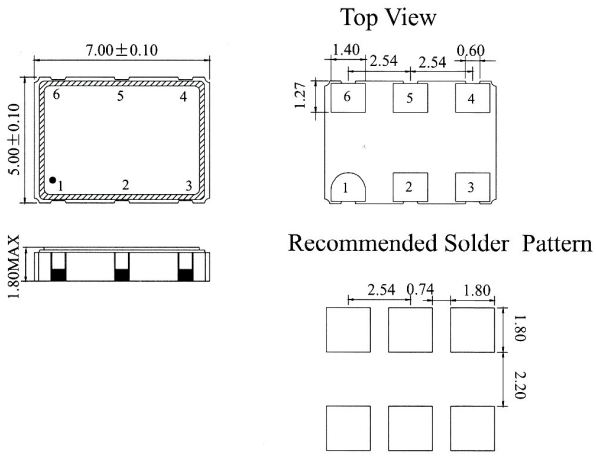
OPTIONS & ORDERING INFORMATION

SX7EJK .... - ..... MHz

Supply Voltage *	Operating Temp. *	Overall Stability *	Tri-state Function	Frequency in MHz
25 = +2.5V **33 =** +3.3V	**E =** 0°/+70°C **F =** -20°/+70°C **K =** -40°/+85°C	**25 =** ±25 ppm **30 =** ±30 ppm **50 =** ±50 ppm **100 =** ±100 ppm	**E =** Tri-state	Please specify the frequency in MHz

* Note : Not all combinations are possible, please consult us.

OUTLINE DIMENSIONS (mm)



Pin Connections	**#1** : E/D	**#2** : NC	**#3**: GND	**#4** : Output
	#5 : Complementary output	**#6**: Vdd		